

Notice of Allowability

Application No.

10/082,490

Examiner

Freshteh N. Aghdam

Applicant(s)

CORY, WARREN E.

Art Unit

2611

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed November 2, 2007.
2. ☒ The allowed claim(s) is/are 1, 3-9 and 11-20.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____ |

DETAILED ACTION

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Justin Liu on January 16, 2008.

As to claim 1, replace the second paragraph of body of said claim with the following paragraph:

A plurality of first level transceivers of the integrated circuit that perform channel bonding operations, each first level transceivers being controlled by the master transceiver and receiving a first control signal by way of a first configurable path, wherein the first configurable path is configured according to a first mode control signal for a first level transceiver, the first mode control signal determining a delay of the first control signal received by way of the first configurable path; and

As to claim 1, replace the third paragraph of body of said claim with the following paragraph:

A plurality of second level transceivers of the integrated circuit that perform channel bonding operations, each second level transceivers being controlled by one of the plurality of first level transceivers and receiving a second control signal by way of a second configurable path, wherein the second configurable path is configured according to a second mode control signal for a second level transceiver, the second mode

control signal determining a delay of the second control signal received by way of the second configurable path;

As to claim 1, replace the last/fourth paragraph of body of said claim with the following paragraph:

wherein each of the plurality of transceivers comprises a controller receiving one of the first mode control signal and second mode control signal designating the transceiver as a master transceiver or a slave transceiver and enabling one of the first and second control signals to be applied to each one of the plurality of transceivers by way of one of the first and second configurable paths during a predetermined clock cycle to enable channel bonding operations for aligning the different portions of the data stream transmitted in parallel.

As to claim 2, cancel claim 2.

As to claim 11, line 1, insert the phrase "plurality of" between the terms "said" and "transceivers".

Allowable Subject Matter

Claims 1, 3-4, 9, and 11-12 are allowed. The following is an examiner's statement of reasons for allowance: The prior art of record fails to disclose a plurality of transceivers in an integrated circuit, and more particularly a plurality of transceivers of an integrated circuit comprising first level transceivers and second level transceivers having different configurable paths, wherein different mode signals determine the delay of control signals in the different configurable paths.

Claims 5-8 and 13-20 are allowed. The prior art of record fails to disclose a first flip-flop that accepts the first input signal and generate an output signal; a first multiplexer having an output terminal and at least a first and a second input terminal, the first input terminal accepting the output signal of the first flip-flop, and the second input terminal being connected to the second input signal; a second flip-flop having an output terminal and an input terminal that connects with the output terminal of the first multiplexer; a second multiplexer having an output terminal and at least a first and a second input terminal, the first input terminal being connected to the output terminal of the second flip-flop, and the second input terminal being connected to the second input signal; and a third flip-flop having an output terminal and an input terminal, the input terminal being connected to the output terminal of the second multiplexer.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Freshteh N. Aghdam whose telephone number is 571-272-6037. The examiner can normally be reached on 9:00-5:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on 571-272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Freshteh N Aghdam
Examiner
Art Unit 2611

January 16, 2008


CHIEH M. FAN
SUPERVISORY PATENT EXAMINER